

USPTO FAX: 571-273-2885

PTO/SB/21 (12-97)

Approved for use through 10/31/2002. OMB 0651-0031

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<h1 style="text-align: center;">TRANSMITTAL FORM</h1> <p style="text-align: center;">(to be used for all correspondence after initial filing)</p>		Application Number	10/743,221
		Filing Date	12/22/2003
		First Named Inventor	Westwick, Alan L.
		Group Art Unit	2817
		Examiner Name	Mottola, Steven J
Total Number of Pages in This Submission	9	Attorney Docket Number	SIL.P0063

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Johnson & Associates Bruce A. Johnson Customer Number 30163
Signature	
Date	January 23, 2006

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Westwick, et al.

Application No.: 10/743,221

Filed: 12/22/2003

Title: CIRCUIT AND METHOD OF
ESTABLISHING DC BIAS LEVELS
IN AN RF POWER AMPLIFIER

Group Art Unit: 2817

Examiner: Mottola, Steven J

Attorney Docket No.: SIL.P0063

LETTER ACCOMPANYING ISSUE FEE PAYMENT

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The following IDS' have been submitted in the present patent application, although initialed forms have not been received by the Applicant(s):

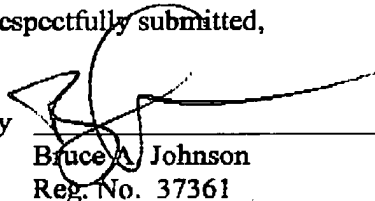
1. 2/13/2005 electronic IDS;
2. 10/20/2005 IDS under; and
3. 10/20/2005 electronic IDS.

Please ensure that the cited references have been considered by the Examiner prior to the issuance of a patent.

For the Examiner's convenience, the IDS forms are attached to this letter.

Respectfully submitted,

By


Bruce A. Johnson
Reg. No. 37361

Date: January 23, 2006

Customer Number 30163
Bruce A. Johnson
Johnson & Associates
PO Box 90698
Austin, TX 78709-0698
Tel. 512-301-9900
Fax 512-301-9915